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FACSIMILE TRANSMITTAL SHEET

TO: Examiner (unassigned)
Group Art Unit 1762

FROM: Kenneth C. Brooks
Reg. No. 38,393

COMPANY:
United States Patent and Trademark Office

DATE:
March 24, 2005

FAX NUMBER:
703-872-9306

TOTAL NO. OF PAGES INCLUDING COVER:
10

RE:

Information Disclosure Statement
U.S. Patent Application No. 10/776,881
Filing Date: 02/11/2004
Entitled: APPARATUS FOR FABRICATING NANOSCALE PATTERNS IN LIGHT CURABLE
COMPOSITIONS USING AN ELECTRIC FIELD
Our Ref. No.: PA84-26D27

Please see attached:

Information Disclosure Statement (2 pg(s)) with
Form PTO/SB/08A (7 pg(s))

KCB:kep

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MAR 24 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: Willson, Carlton Grant
App. No.: 10/776,881 GPAU: 1762
Filing Date: February 11, 2004 Examiner: (unassigned)
Dkt. No.: PA84-26D27 Conf. No.: 3386
For: APPARATUS FOR FABRICATING NANOSCALE PATTERNS IN LIGHT
CURABLE COMPOSITIONS USING AN ELECTRIC FIELD

INFORMATION DISCLOSURE STATEMENT

Mail Stop AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The following information is submitted in compliance with Applicants' duty of disclosure and pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98. The undersigned brings the patents, publications, applications or other information identified in the attached:

☒ Form(s) PTO/SB/08A and/or PTO/SB/08B or PTO/1449

☐ Other:

to the Examiner's attention in the above-identified application. It is respectfully requested that the cited information be expressly considered during the prosecution of this application, and the references be made of record therein and appear among the "references cited" on any patent to issue therefrom.

For the convenience of the Examiner, the references that were cited in the parent patent application are noted in bold.

This Information Disclosure Statement is being filed:

before the mailing date of a first Office action on the merits or before the mailing date of a first Office action after the filing of a request for continued examination under § 1.114. Therefore, no fee is believed required.

Applicants do not believe that any additional fees are due, but if the Commissioner believes additional fees are due, the Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account Number 502650.

CERTIFICATE OF TRANSMISSION
I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as either First Class Mail or Express Mail, in an envelope addressed to the Commissioner for Patents.

Signed: Katrina Prati
Typed Name: Katrina Prati
Date: 3-24-05

Respectfully Submitted,



Kenneth C. Brooks,
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Legal Department
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PTO/SB/08A (08-03)

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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				APPLICATION NUMBER	10/776,881
				FILING DATE	02/11/2004
				FIRST NAMED INVENTOR	Willson et al.
				Group Art Unit	1762
				Examiner Name	Unassigned
Sheet 1 of 7				Attorney Docket Number	PAB4-26D27

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ⁴ (if known)			
	C1	4,731,155		Napoli et al.	3/15/1988	
	C2	5,425,848		Halsma et al.	6/20/1995	
	C3	5,772,905		Chou	6/30/1998	
	C4	6,309,580	B1	Chou	10/30/2001	
	C5	6,334,960	B1	Willson et al.	1/1/2002	
	C6	6,517,977	B2	Resnick et al.	2/11/2003	
	C7	6,696,220	B2	Bailey et al.	2/24/2004	
	C8	2004/0007799	A1	Choi et al.	1/15/2004	
	C9	2004/0021254	A1	Sreenivasan et al.	2/5/2004	
	C10	2004/0021866	A1	Watts et al.	2/5/2004	
	C11	2004/0022888	A1	Sreenivasan et al.	2/5/2004	
	C12	2002/0177319	A1	Chou	11/28/2002	
	C13	2003/0215577	A1	Willson et al.	11/20/2003	
	C14	2004/0008334	A1	Sreenivasan et al.	1/15/2004	
	C15	2004/0009673	A1	Sreenivasan et al.	1/15/2004	
	C16	2004/0124566	A1	Sreenivasan et al.	7/1/2004	
	C17	6,646,662	B1	Nebashi et al.	11/11/2003	
	C18	6,778,094	B1	Whitesides et al.	8/17/2004	

Examiner Signature		Date Considered	
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Sheet	2	of	7	Examiner Name	Unassigned
				Attorney Docket Number	PA84-26D27

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		Number	Kind Code ² (if known)			
	C19	6,518,189	B1	Chou	2/11/2003	
	C20	2002/0132482	A1	Chou	9/19/2002	
	C21	2002/0167117	A1	Chou	11/14/2002	
	C22	6,482,742	B1	Chou	11/19/2002	
	C23	2003/0080471	A1	Chou	5/1/2003	
	C24	2004/0036201	A1	Chou et al.	2/26/2004	
	C25	2004/0192041	A1	Jeong et al.	9/30/2004	
	C26	6,828,244	B2	Chou	12/7/2004	
	C27	6,809,356	B2	Chou	10/26/2004	
	C28	2004/0197843	A1	Chou et al.	10/7/2004	
	C29	2004/0156108	A1	Chou et al.	8/12/2004	
	C30	2004/0137734	A1	Chou et al.	7/15/2004	
	C31	2004/0131718	A1	Chou et al.	7/8/2004	
	C32	2004/0118809	A1	Chou et al.	6/24/2004	
	C33	2004/0046288	A1	Chou	3/11/2004	
	C34	2003/0080472	A1	Chou	5/1/2003	
	C35	2003/0034329	A1	Chou	2/20/2003	
	C36	2002/0042027	A1	Chou et al.	4/11/2002	

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PAGE 6/10 * RCVD AT 3/24/2005 5:55:12 PM [Eastern Standard Time] * SVR:USPTO-EFXRF-1/2 * DNIS:8729306 * CSID:5124918918 * DURATION (mm:ss):03:32

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				FIRST NAMED INVENTOR	Willson et al.	
				Group Art Unit	1762	
				Examiner Name	Unassigned	
Attorney Docket Number	PA84-26D27					
Sheet	4	of	7			

FOREIGN PATENT DOCUMENTS							
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		Office ³	Number ⁴				
	C47	JP	02-92603	Kurikawa et al.	4/3/1990		
	C48	JP	02-24848	Kamio	1/26/1990		
	C49	WO	01/47003	Steiner et al.	6/28/2001		
	C50	WO	02/07199	Chou	1/24/2002		
	C51	WO	2004/114016	Chou et al.	12/29/2004		
	C52	WO	99/05724	Chou et al.	2/4/1999		
	C53	WO	03/010289	Chou et al.	2/6/2003		
	C54	WO	03/079416	Chou	9/25/2003		
	C55	WO	03/099536	Chou et al.	12/4/2003		

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(use as many sheets as necessary)				FIRST NAMED INVENTOR	Willson et al.
				Group Art Unit	1762
Sheet	5	of	7	Examiner Name	Unassigned
				Attorney Docket Number	PA84-26D27

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²

	C56	Chou, Nanoimprint Lithography and Lithographically Induced Self-Assembly, MRS Bulletin, pp. 512 - 517, 7/1/2001	
	C57	Choi et al., Design of Orientation Stages for Step and Flash Imprint Lithography, Precision Engineering, pp. 192 - 199, 1/1/2001	
	C58	Colburn et al., Step and Flash Imprint Lithography for sub-100 nm Patterning, Proceedings of SPIE Vol. 3997, pp. 453 - 457, 1/1/2000	
	C59	Chou et al., Imprint Lithography with 25-Nanometer Resolution, Science Vol. 272, pp. 85 - 87, 4/5/1996	
	C60	Chou et al., Imprint Lithography with Sub-10 nm Feature Size and High Throughput, Microelectronic Engineering 35, pp. 237 - 240, 1/1/1997	
	C61	Haisma et al., Mold-assisted Nanolithography: A Process for Reliable Pattern Replication, J. Vac. Sci. Technol. B, pp 4124 - 4128, 11/1/1996	
	C62	Chou et al., Imprint of Sub-25 nm Vias and Trenches in Polymers, Appl. Phys. Lett. 67 (21), 11/20/1995	
	C63	Johnson et al., Advances in Step and Flash Imprint Lithography, SPIE Microlithography Conference, 2/23/2003	
	C64	Chou et al., Lithographically Induced Self-assembly of Periodic Polymer Micropillar Arrays, J. Vac. Sci. Technol. B 17 (6), pp. 3197 - 3202, 11/1/1999	

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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
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	C65	Colburn et al., Step and Flash Imprint Lithography: A New Approach to High-Resolution Patterning, Proc. Of SPIE, vol. 3676, 3/1/1999	
	C66	Heidari, Nanoimprint Lithography at the 6 in. Wafer Scale, J. Vac. Sci. Technol. B 18(6), pp. 3557 - 3560, 11/1/2000	
	C67	Translation of Japanese Patent 02-92603	
	C68	Translation of Japanese Patent 02-24848	
	C69	Chou et al., Ultrafast and Direct Imprint of Nanostructures in Silicon, Nature, Vol. 417, (June 2002), pp. 835-837, 6/1/2002	
	C70	Chou et al., Nanoimprint Lithography, Journal of Vacuum Science Technology B 14(16), pp. 4129-4133, 11/1/1996	
	C71	Colburn et al., Development and Advantages of Step-and-Flash Lithography, Solid State Technology, 7/1/2001	
	C72	Colburn et al., Characterization and Modeling of Volumetric and Mechanical Properties for Step and Flash Imprint Lithography Photopolymers, Journal of Vacuum Science Technology. Vol b. 19(6), 11/1/2001	
	C73	Bailey et al., Step and Flash Imprint Lithography: Defect Analysis, Journal of Vacuum Science, B 19(6), pp. 2806-2810, 11/1/2001	

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	C74	Bailey et al., Step and Flash Imprint Lithography: Template Surface Treatment and Defect Analysis, Journal of Vacuum Science, B 18(6), pp. 3572-3577, 11/1/2000	
	C75	Schneider et al., Stripes of Partially Fluorinated Alkyl Chains: Dipolar Langmuir Monolayers	

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